

**DISCIPLINE SPECIFIC CORE COURSE – DSC - 15:
DIGITAL ELECTRONICS**

Course Title & Code	Credits	Credit distribution of the course			Eligibility Criteria	Pre-requisite of the course
		Lecture	Tutorial	Practical		
Digital Electronics DSC – 15	4	3	0	1	Class XII pass with Physics and Mathematics as main subjects	NIL

SYLLABUS OF DSC - 15

THEORY COMPONENT

Unit – I - Integrated circuits (2 Hours)

Integrated Circuits (Qualitative treatment only), active and passive components, discrete components, wafer, chip, advantages and drawbacks of ICs, scale of integration: SSI, MSI, LSI and VLSI (basic idea and definitions only), classification of ICs, examples of linear and digital ICs

Unit – II - Digital circuits and Boolean algebra (14 Hours)

Difference between analog and digital circuits, binary number, decimal to binary and binary to decimal conversion, BCD, octal and hexadecimal numbers, AND, OR and NOT gates (realization using diodes and transistor), NAND and NOR gates as universal gates, XOR and XNOR gates and application as parity checkers

De Morgan's theorems, Boolean laws, simplification of logic circuit using Boolean algebra, fundamental products, idea of minterms and maxterms, conversion of truth table into equivalent logic circuit by (1) Sum of Products method and (2) Karnaugh map simplification (upto four variables).

Unit – III - Combinational Logic Circuits (9 Hours)

Data processing circuits: Multiplexers and its applications, de-multiplexers, decoders, encoders
Arithmetic logic circuits: Express binary number in signed and unsigned form, 1's and 2's complement representation, binary addition, binary subtraction using 2's complement, half and full Adders, half and full subtractors, 4-bit binary adder/subtractor using 2's complement method.

Unit – IV - Sequential Logic Circuits (8 Hours)

Flip Flops SR, D, and JK clocked (level and edge triggered) flip-flops, preset and clear operations, race-around conditions in JK flip-flop, master-slave JK flip-flop, conversion of one flip flop to another using an excitation table

Unit – V - Application of Sequential Logic Circuits (9 Hours)

Shift registers: Serial-in-Serial-out, Serial-in-Parallel-out, Parallel-in-Serial-out and Parallel-in-Parallel-out Shift Registers (only up to 4 bits).

Counters: Asynchronous counters, MOD-N synchronous counter designing using excitation table.

Unit – VI – Timers (3 Hours)

IC 555: Pin -out diagram, block diagram and its applications as astable multivibrator and monostable multivibrator

- 1) Digital Principles and Applications, A. P. Malvino, D. P. Leach and Saha, 7th edition, 2011, Tata McGraw
- 2) Fundamentals of Digital Circuits, A. Kumar, 2nd edition, 2009, PHI Learning Pvt. Ltd.